



**LAKIREDDY BALI REDDY COLLEGE OF ENGINEERING
(AUTONOMOUS)**

L.B. Reddy Nagar :: Mylavaram-521 230 :: Krishna Dist. :: A.P
Approved by AICTE, New Delhi. Affiliated to JNTUK, Kakinada
Accredited by NAAC with "A" Grade & ISO 9001: 2015 Certified
EXAMINATION SECTION

**B.Tech.(I-Semester)(R17)
(2017 Admitted Batch)**

A.Y. 2017-2018


II - ONLINE QUIZ EXAMINATION

TIME TABLE

DATE	AE	CE	CSE	ECE	EEE	EIE	IT	ME
13-11-2017 Monday	17FE01 - Professional Communication-I	17FE01 - Professional Communication - I	17FE01 - Professional Communication-I	17FE01 - Professional Communication-I	17FE01 - Professional Communication-I	17FE01 - Professional Communication-I	17FE01 - Professional Communication - I	17FE01 - Professional Communication-I
14-11-2017 Tuesday	17FE04 - Differential Equations and Linear Algebra	17FE04 - Differential Equations and Linear Algebra	17FE05 - Differential Equations and Numerical Applications	17FE04 - Differential Equations and Linear Algebra	17FE04 - Differential Equations and Linear Algebra	17FE04 - Differential Equations and Linear Algebra	17FE04 - Differential Equations and Linear Algebra	17FE04 - Differential Equations and Linear Algebra
15-11-2017 Wednesday	17FE13 - Engineering Physics	17FE13 - Engineering Physics	17FE15 - Engineering Chemistry	17FE15 - Engineering Chemistry	17FE12 - Applied Physics	17FE15 - Engineering Chemistry	17FE15 - Engineering Chemistry	17FE13 - Engineering Physics
16-11-2017 Thursday	17CI01 - Computer Programming	17CI01 - Computer Programming	17CI01 - Computer Programming	---	17CI01 - Computer Programming	17CI01 - Computer Programming	17CI01 - Computer Programming	17CI01 - Computer Programming
17-11-2017 Friday	---	17CE01 - Building Materials and Construction	17EC02 - Electronic Devices and Circuits	17EC02 - Electronic Devices and Circuits	17ME50 - Basic Engineering Mechanics	---	17EC02 - Electronic Devices and Circuits	---
18-11-2017 Saturday	---	---	---	17EC01 - Electrical Circuits and Networks	---	17EC01 - Electrical Circuits and Networks	---	---

Date: 04-11-2017

- Copy to: 1. All Notice Boards
2. All H.O.Ds for N.A.,


 04/11/2017
CONTROLLER OF EXAMINATIONS


PRINCIPAL

(Contd.)



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EXAMINATION SECTION

Phone & Fax.: 08659-223498

examsection@lbrce.ac.in

**B.Tech.(I-Semester)(R17)
 (2017 Admitted Batch)**

Date : 04-11-2017

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II - ONLINE QUIZ EXAMINATION

TIME TABLE

Following is the schedule for B.Tech (I Semester) (R17 batch) II - online quiz examinations.

DATES	BRANCH		VENUE
	SLOT-1 (12.15 p.m - 12.45 p.m)	SLOT-2 (12.45p.m - 01.15 p.m)	
13-11-2017	CSE (A/S)	--	IPC lab (Block-I) Ground Floor
	CSE (B/S)	--	IT workshop (Block-I) Ground Floor
	--	IT	CDS lab (Block-I) First Floor
	--	ME (C/S)	IT lab (Block-I) First Floor
	EEE (A/S)	EEE (B/S)	Computational lab (Block-II) First Floor
18-11-2017	ME (A/S)	ME (B/S)	CAD/CAM lab (Block-III) Ground Floor
	ECE (A/S)	ECE (B/S)	SP lab (Block-IV) Second Floor
	EIE	ECE (C/S)	Systems lab (Block-V) Ground Floor
	CE	AE	English lab (Block-V) First Floor

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04/11/2017